



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Yuri Mirgorodski et al.

Application No.: 10/664,469

Filed: September 17, 2003

For: A METHOD OF PMOS STACKED-
GATE MEMORY CELL
PROGRAMMING ENHANCEMENT
UTILIZING STAIR-LIKE PULSES OF
CONTROL GATE VOLTAGE

Confirmation No.: Unknown

Group Art Unit: Unknown

Examiner: Unknown

SUBMISSION OF FORMAL DRAWINGS

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CERTIFICATE OF MAILING

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STALLMAN & POLLOCK LLP

Dated: 12/10/03

By: 

Janet Chan

Sir:

Enclosed are two (2) sheets of formal drawings for filing in the above-referenced application.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: December 10, 2003

By: 

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